The Cause and Eliminate Method of Self-excited Oscillation in Long Cable Amplifier Circuits

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Abstract: SMA long cable distributed capacitance is easy to trigger broadband amplifier circuit’s self-oscillation. In this study, we design an amplifier circuit based on OPA691 chip, discuss the influence of long cable’s distributed capacitance, then prompt a solution to add 50Ω compensation resistor between the output terminal and the load capacitance. According to the Tina simulation analysis, when 50Ω compensation resistor is added, bode diagrams show amplitude characteristic is smaller than 0 dB at -180 ° phase. The self-oscillation also disappeared in actual circuit measurement, which verify the rationality of this solution.

1 INTRODUCTION

Most of the operational amplifier circuit inevitably contains capacitive load components, such as distributed capacitance of the PCB conductor or coaxial cable, these objective existence will affect the operational amplifier characteristics, in the case of broadband it easily trigger instability work, or even self-oscillation phenomenon. This paper will explore the principle and the eliminate measures of broadband amplifier self-excited oscillation caused by SMA long cable distribution capacitor.

2 PRINCIPLE OF SELF-EXCITED OSCILLATION

Fig.1 shows a schematic diagram of a feedback amplifier. \( \hat{A} \) is the amplification factor of basic amplifier, \( F \) is the feedback coefficient of the feedback network.

\[ F = \frac{X_v}{X_i} = \frac{X_0}{X_0 + X_0R} = \frac{1}{1 + AF} \]  \hspace{1cm} (1)

The self-excited phenomenon must meet the self-oscillation conditions, feedback loop coefficient: \( AF = 1 \). The conditions consist of two parts:

\begin{itemize}
  \item[a.] Amplitude condition is: \( |AF| = 1 \)
  \item[b.] Phase condition is:
  \[ \angle AF = \phi_A + \phi_F = (2n+1)\pi \quad (n=0,1,2\cdots) \]

Where \( \phi_A \) is the phase of the amplifier \( \hat{A} \), \( \phi_F \) is the phase of the feedback network \( F \), \( \phi_A + \phi_F \) is the phase difference between the feedback signal and the input signal. When actual circuit meets the phase condition and positive feedback, also satisfy \( \phi_A + \phi_F \in \left(\frac{(2n+1)\pi}{4}, \frac{(2n+1)\pi}{4}+\frac{\pi}{4}\right) \) (\( n=0,1,2\cdots \)), the circuit is in an unstable state, which may also produce self-oscillation\textsuperscript{[2]}.

The physical meaning of the above description is: If normalized vector signals which enter the broadband amplifier at one moment into a signal unit, then the broadband amplifier will output the corresponding vector of 1 \( \times \hat{A} \) units; and then through the feedback network \( F \) back to the broadband amplifier, will return corresponding vector of 1 \( \times \hat{A} \times \hat{F} \) units; if 1 \( \times \hat{A} \times \hat{F} = 1 \), it means that without external input signals, the feedback signal can maintain in the reciprocal transfer feedback path, keeping the same energy \textsuperscript{[3]}. This means that the system can output equal amplitude oscillations without input -that is, self-excited oscillation.

3 SMA LONG CABLE DISTRIBUTED CAPACITANCE’S IMPACT ON OPERATIONAL AMPLIFIER

SMA coaxial cable’s center coaxial conductor and outer shield metal can be equivalent to the distributed capacitance as shown in Fig.2. Refer to the manufacturer’s information, we use SMA distributed capacitance about 96.1 pf/m. So introduce the SMA cable equivalent of increasing the capacitive load of broadband amplifier.

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circuit, and impact the stability of the entire amplifier system\[3\].

$$\begin{align*}
\text{Vin} & \rightarrow R_1 \rightarrow R_2 \rightarrow \text{SMA cable} \\
\text{R}_3 & = 1 \text{M}
\end{align*}$$

\[\text{Equivalent circuit} \ R_3 = 1 \text{M} C\]

**Figure 2:** Distributed capacitance equivalent model

In 1.5m line simulation, the distribution of capacitance value is similar to 96.1pf / m * 1.5m ≈ 150pf; the broadband amplifier circuit using TI's OPA691 chip (set the Rf bandwidth to 260MHz or less) to build 8 times the magnification of the in-phase amplification circuit. OPA691 open-loop gain characteristics determine the amplitude characteristic of $\dot{A} = \frac{R_1}{R_1 + R_2}$. Refer the OPA691 manual comprehensive analysis Fig.2 circuit $\dot{A}$ can be obtained in this ideal circuit should be stable, will not occur self-oscillation. This is also demonstrated by circuit simulation using the TINA simulation software provided by TI’s official website, as it shows in Fig.3. However, after considering the influence of the distributed capacitance, the circuit is unstable and may occur self-oscillation in Fig.4.

### 3.1 SIMULATION OF AMPLITUDE-FREQUENCY CHARACTERISTIC WITHOUT CAPACITIVE LOAD

In the case of uncharged load, analogy the capacitance model of Fig.2 can simulated the circuit shown in Fig.3 left, the pote characteristic is shown in Fig.3 right.

**Figure 3:** Circuit and baot diagram without capacitive load

In the self-excited oscillation conditions, when $|\dot{A}| < 1$, the output signal is continuously reduced, and will not occur self-oscillation. In the frequency of -180 ° phase, the amplitude characteristic is -5.94dB <0dB, the system is stable, will not occur self-oscillation. [Note: set Rf OPA691 bandwidth within 260MHz, beyond this frequency will not occur self-oscillation, the specific derivation refer to OPA691 official website datasheet description\[4\].]

### 3.2 SIMULATION OF AMPLITUDE-FREQUENCY CHARACTERISTICS WITH CAPACITIVE LOAD

The following circuit adds the equivalent capacitance to the SMA connected position\[5\]. The 2m cable introduces about 200pf equivalent capacitance. The effect is shown in Fig.4.

**Figure 4:** Circuit and baot diagram with capacitive load

From the amplitude-frequency characteristic curve shows that the phase at -180 °, there is 142.04MHz frequency amplitude characteristics 8.46dB > 0dB, to meet the conditions of self-oscillation occurred. Therefore, in the case of 150pF capacitive load, instability will produce self-oscillation.

Through the above comparison simulation is obvious, capacitive load $C_7$ affect the system’s amplitude-frequency characteristics, undermine the stability of the system.

### 4 ANALYZE THE THEORY

#### 4.1 CREATE A CIRCUIT MODEL

To analyze the principle of the capacitance, the equivalent circuit of the amplifier circuit in Fig.4 must be analyzed. Because in fact the amplifier open-loop output resistance is not 0Ω, so the first broadband amplifier OPA691 equivalent to for the flow control voltage source with a amplification factor is $\dot{A}$, the external output contains a resistance Ro, the official manual indicate $Ro \approx 15 \Omega$, as shown in Figure 5.

**Figure 5:** Amplifier equivalent circuit

Open-loop simulate the OPA691 circuit, using 1GH inductance $L_1$ to redisconnect the AC loop, and through the 1GF introduce $C_8$ the AC ground attach to the inverting
input, with 1MΩ resistor load, then get the corresponding baud figure as shown in Fig.6.

Figure 6: Open-loop simulate circuit and the baot diagram

4.2 CALCULATION

In the actual simulation, we simulate the capacitive load and the no-capacitive load separately, find out the problem by contrast and solve the problem

4.2.1 NO-CAPACITIVE LOAD ANALYSIS

When the capacitive load $C_1$, the simulation shown in Figure 3. Theoretical calculation implies the feedback coefficient is a pure real number, phase change of $\angle \Delta F$ only determined by the amplifier amplitude frequency characteristics $\mathcal{A}$

$\mathcal{F} = 50/(350 + 50) \approx 1/\beta \quad \angle \mathcal{F} = 0$ (2)

$20 \log_{10}|\Delta F| = 20 \log_{10}|\mathcal{A}| - 20 \angle \Delta F = \angle \mathcal{A}$ (3)

Add the $\beta = 1/\mathcal{F} = 20\text{dB}$ curve in the open-loop gain spectrum in Fig.6, the two-point intersection is $20 \log_{10}|\Delta F| = 0\text{dB}$ frequency. The phase at this frequency point is -127.63° and does not occur self-oscillation.

4.2.2 CAPACITIVE LOAD ANALYSIS

With the load of capacitive $C_1$, the equivalent diagram shown in Fig.7 left side.

Theoretical calculation

$V_o = \mathcal{A} \cdot V_i \cdot \left(\frac{1}{\mathcal{F} \mathcal{C}_l} / (R_f + R_o)\right) \cdot \left(\frac{1}{\mathcal{F} \mathcal{C}_l} / (R_f + R_o)\right)$

When $R_C \gg R_o$, $R_f \gg R_o$, and $R_f \gg R_C$, the approximate calculation can ignored $R_C$, $R_f$, $R_o$ as shown in the right side of Fig.7, $V_o \approx \mathcal{A} \cdot V_i \cdot \left(\frac{1}{\mathcal{F} \mathcal{C}_l} / (R_f + R_o)\right)$

The feedback voltage $V_o \approx A \cdot V_i \cdot \left(\frac{1}{\mathcal{F} \mathcal{C}_l} / (R_f + R_o)\right)$ is equivalent to adding a low-pass filter based on the 4.2.1 analysis, which means adding a pole.

Figure 7: Equivalent circuit and simplified diagram

According to Fig.3 and Fig.4, it can be observed that there is one more pole in Fig. 4 than Fig.3, and an inflection point occurs at a frequency of 70.9 MHz. The verification of this pole is:

$$f_p = \frac{1}{2\pi R_o C_s} \quad (4)$$

When $R_o \approx 15 \Omega$, $C_s \approx 150\text{pF}$, calculate the pole frequency is about 71MHz, basically same as the simulation results.

5 SELF-EXCITED OSCILLATION ELIMINATION

The reason for self-oscillation occurs after capacitive load added is that pole produced damage the stability of the circuit. So we need to introduce a zero point after the main pole position, thus suppressing the influence of the pole and maintaining the stability of the circuit.[6]

As equivalent circuit shows on the left of Fig.8, resistor $R_s=50$ is placed outside the loop of the amplifier, then connected to the load $C_7$ capacitor.

Figure 8: Compensation circuit and simplified diagram

According to the calculate formula:

$V_o \approx A \cdot V_i \cdot \left(\frac{1}{\mathcal{F} \mathcal{C}_l} / (R_f + R_s)\right) \cdot \left(\frac{1}{\mathcal{F} \mathcal{C}_l} / (R_f + R_s)\right)$

When $R_s \gg (R_f + R_o)$, $R_f \gg R_o$, $R_s \gg (R_f + R_o)$, approximate calculations can ignore $R_s$, $R_f$, $R_o$, as shown in Fig.8 on the right.

$V_o \approx A \cdot V_i \cdot \left(\frac{1}{\mathcal{F} \mathcal{C}_l} / (R_f + R_s)\right) \cdot \left(\frac{1}{\mathcal{F} \mathcal{C}_l} / (R_f + R_s)\right)$

So, the feedback voltage is:

$V_o = V_0 \cdot \mathcal{A} \cdot V_i \cdot \left(\frac{1}{\mathcal{F} \mathcal{C}_l} / (R_f + R_s)\right) \cdot \left(\frac{1}{\mathcal{F} \mathcal{C}_l} / (R_f + R_s)\right)$

This is equivalent to adding a zero point on the basis of 4.2.2 analysis.

$$f_{z1} = \frac{1}{2\pi R_o C_s} \quad (5)$$

After adding $R_s$, the position of $f_p$ produced by $R_f$ and $C_l$ unchanged, now it is determined by $R_f$, $R_s$ and $C_s$:

$$f_{z2} = \frac{1}{2\pi \times (R_s + R_o) C_s} \approx 16.3\text{MHz} \quad (6)$$

At the same time introduce a zero point $f_{z2}$ after the first pole to suppress the impact of the pole on the system, which location determined by $R_s$ and $C_s$:

$$f_{z3} = \frac{1}{2\pi R_s C_s} \approx 21.2\text{MHz} \quad (7)$$

Simulation result shows in Fig.9:
Figure 9: Compensation circuit model

From the amplitude-frequency characteristic curve shows, there is 1.25GHz frequency amplitude characteristics -12.55dB <0dB at -180 ° phase, and set RF below 260Mhz, there is no frequency instability and positive feedback phenomenon. It proves this method can successfully eliminate the self-oscillation.

6 CIRCUIT MEASUREMENT

The test circuit shown in Fig.10, in the actual measurement, Vf2 grounded through 50Ω resistor, Vf1 through the SMA cable connected to the oscilloscope which input probe impedance> 1MΩ.

Figure 10: Test circuit

Test equipment: Agilent DSO7054A oscilloscope;
Test power supply: ± 5V DC voltage source;
Test cable: ancient wave semi-flexible cable with different length of 2.2m, 1.8m, 1.5m, 1.2m, 80cm, 60cm.
Results of testing:
Connect different lengths of cable, the capacitance parameters will change, test results shown in Table 1.

<table>
<thead>
<tr>
<th>Length /m</th>
<th>Equivalent capacitance /pf</th>
<th>Frequency /Mhz</th>
<th>Self-oscillation After introduce 50Ω resistor (Yes/No)</th>
</tr>
</thead>
<tbody>
<tr>
<td>2.2</td>
<td>211.4</td>
<td>64.5</td>
<td>No</td>
</tr>
<tr>
<td>1.8</td>
<td>173.0</td>
<td>76.1</td>
<td>No</td>
</tr>
<tr>
<td>1.5</td>
<td>144.15</td>
<td>90.4</td>
<td>No</td>
</tr>
<tr>
<td>1.2</td>
<td>115.32</td>
<td>36.7</td>
<td>No</td>
</tr>
<tr>
<td>0.8</td>
<td>76.9</td>
<td>50.5</td>
<td>No</td>
</tr>
<tr>
<td>0.6</td>
<td>38.4</td>
<td>63.8</td>
<td>No</td>
</tr>
</tbody>
</table>

According to the summary table, it can be clearly concluded that adding a capacitive load causes the amplifier system generate self-oscillating oscillation. By introducing a 50Ω series resistor between the output of the op amp and the load capacitor, the self-oscillating oscillation can be eliminated.

7 SUMMARY

This paper analyzes the self-oscillation of the broadband amplifier caused by SMA long cable distribution capacitance, explain and simulate the reason of the self-oscillation of the circuit with capacitive load. At the same time propose the method of solving this problem, that is, introducing a 50Ω series resistor between the output of the op amp and the load capacitor to eliminate the self-excited oscillation. The method proved feasibility in both theory and actual test in various lengths cable.

REFERENCES