Digital control systems for power supplies of space vehicles

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Abstract. The principle of building fault-tolerant digital control systems for power supplies of space vehicles is considered in the article. The analysis of the circuits by sampling the controlled component of the output voltage at discrete instants of time is carried out. The possibility of using this approach in multi-channel digital control systems is shown. The results of theoretical studies are confirmed by the example of a four-channel control unit.

1 Introduction

Widely used analog control systems for switching voltage regulators (SVR) have a number of significant disadvantages: insufficient flexibility in control, impossibility of implementing complex control algorithms, low noise immunity and limited accuracy due to leakage currents, zero-point voltages, temperature of parameter instability, etc.

The use of digital control systems (DCS) improves the parameters of the SVRs, which will provide the following opportunities:
1) selection of the optimal operating modes of the power elements (switching frequency of power switches, change of induction in magnetic elements, current ripple in inductive elements and voltage across capacitors, etc.) [1];
2) the introduction of mathematically complex transfer functions (control laws), their modification without redesigning the equipment;
3) maintenance and issuance of signal information on the load, primary power supply, dangerous operating modes, etc.;
4) realization of intellectual functions of adaptation, self-diagnostics and self-adjustment;
5) forecasting catastrophic regimes;
6) the organization of any interfaces for modern consumers and many other functions [2-4].

Digitization of control algorithm makes it possible to implement the most promising predictive algorithms (predictive control) of frequency control by the pulse width modulation (PWM), which allows reducing the delays in the control loop and the delays of information transfer.

2 Problem statement

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When developing PWM switching voltage regulator in power supply systems (PSS) of space vehicles (spacecraft), the task of researching dynamic characteristics and forming optimal control laws in the "small" signal mode is put on the foreground. Using the approach proposed in [5], it is possible to study dynamics from continuous models, and quantization errors can be estimated from the stationary periodic regime. However, at the synthesis of systems using such continuous models the possibilities of pulse regulation are not used. Other approaches involve taking into account the discrete nature of processes in PWM systems by means of data reduction to the models with pulse-amplitude modulation (PAM) [6], or to the models with a variable structure in the state space [7].

3 Problem-solving methods

Of all the known approaches to the synthesis of control laws that ensure high speed with stabilization of the output voltage, a special method is of interest:
- representation of the power circuits of the switching voltage regulators with PWM in the mode of small deviations as an adequate amplitude-impulse model with the controlled components of the processes;
- synthesis of the control law using polynomial equations for the systems with PAM;
- implementation of the synthesized control law in switching voltage regulator taking into account specifics of PWM.

This method allowed its authors to synthesize a control law that provides the minimum finite duration of transient processes in the switching voltage regulator based on DC-DC buck converter. Such a law is called optimal by the authors for speed, and a voltage regulator with such a control law is a high-speed voltage regulator. In the proposed approach to the synthesis of the control law was further developed for both boost and buck-boost DC-DC converters.

In accordance with the discrete transfer function of the synthesized correction device and taking into account the specifics of PWM, two ways to implement the PWM control device are proposed:
- on instantaneous values of state variables;
- on the values of state variables, selected at discrete instants of time.

\[ W_K(p) = d + d \left(1 - e^{-pT}\right) \]  

(1)

where \( d = LC/T \), \( L \) and \( C \) are the inductance and capacitance of the output filter of switching voltage regulator.

The implementation of the PWM control law, in which only discrete values of the regulated component of the voltage across the capacitor of the output filter are used, is shown in Fig. 1.

**Fig 1.** Switching voltage regulator with discrete control
In this scheme, the increment of the input signal of the pulse-width modulator is determined from the expression:

$$\Delta U_y(mT) = -\frac{d}{U_{in}} \left[ 2\Delta U_{Cp}(mT) - \Delta U_{Cp}(m-1)T \right],$$  (2)

where $U_{in}$ is the voltage at the input of the scheme; $K_m = \Delta t_{c,p} / \Delta U_y(mT)$, $\Delta t_{c,p}$ - increment of the pulse duration of the control pulse of the power switch of the regulator with respect to the time $mT$ of the controlled switching; $\Delta U_y(mT)$ is the increment of the PWM input signal; $U_m$ is the amplitude of the sawtooth PWM voltage, $T$ is the conversion period.

Given the condition $\Delta t_{c,p} < T$, it can be assumed that the controlled switching of the power switch of the voltage regulator occurs with a period of $T$.

The current values of the dynamic component of the input signal of the pulse-width modulator are given by:

$$U_y(mT) = U_y((m-1)T) + \Delta U_y(mT),$$  (3)

It is impossible to determine the regulated component of the voltage $U_{Cp}$ across the capacitor of the output filter of the regulator by performing computational operations with discrete or continuous values of the output voltage $U_{out}$ of the scheme. This is explained by the fact that the equivalent circuit for the capacitor of the output filter can be represented as a series connection of capacitance $C_f$ and internal resistance $R_C$ of a capacitor. Therefore, the output voltage of regulator which is the voltage across the capacitor of the output filter of scheme consists of the voltage across the capacitance $U_C$ and the voltage across the internal resistance $U_{Rc}$.

Since the internal resistance $R_C$ of the capacitor can significantly change under the influence of temperature and time factors, then the voltage $U_{Rc}$ will also change, and therefore, to determine the voltage across the capacitance $U_C$ for example, by subtracting the undefined voltage $U_{Rc}$ from the output voltage $U_{out}$ is not possible.

It is possible to calculate the increment of the regulated component of the voltage $\Delta U_{Cp}$ across the capacitance over the period $T$ by integrating the increment of the regulated component of the capacitor current $\Delta I_{Cp}$ in the time interval equal to the period $T$:

$$\Delta U_{Cp}(mT) = \frac{1}{C} \int_{(m-1)T}^{mT} \Delta I_{Cp}((m-1)T)dt,$$  (4)

Since the increment of the regulated component of the capacitor current in the interval between the adjustable switching times $mT$ of the power switch of the voltage regulator
remains unchanged (Fig. 1), then to determine it the first difference in the capacitor current should be calculated:

$$\Delta I_{Cp}(mT) = I_{Cp}(mT) - I_{Cp}((m-1)T)$$

or

$$\Delta I_{Cp}(mT) = I_{Cp}(mT + \tau) - I_{Cp}((m-1)T + \tau), \quad (5)$$

where $\tau < T$ is, in general case, an arbitrarily chosen fixed time interval.

In order to have the time for carrying out the computational procedures necessary to determine the input signal of the pulse-width modulator at the instant of time $mT$, it is advisable to select $\tau$ such a way that the moments of time $(m-1)T + \tau$ are as far removed from the moments $mT$ of the adjustable switching of the power switch of voltage regulator. For DC-DC buck converter and for modulation of the fall time of the pulse, the time instants $(m-1)T + \tau$ should be selected immediately after the power switch is turned on.

As a rule, the static duty cycle $K_{St} > 0.25$ in the buck DC-DC converters, therefore, the sampling time of the discrete values of the input signals and the computation is still close to a quarter of the conversion period. Using expression (1.5) allows us to define $\Delta I_{Cp}(m-1)T + \tau$ at time $(m-1)T + \tau$.

Since the increments of the regulated component of the voltage in the interval between the adjustable switching times of the power switch of the voltage regulator are constant, then (1.4) can be written as:

$$\Delta U_{Cp}(mT) = \frac{1}{C} \Delta I_{Cp}((m-1)T), \quad (6)$$

Thus, replacing the integration procedure in accordance with (1.4) by determining the area of the rectangle according to (1.6) allows us to determine the increment of the regulated component of the voltage across the capacitance $\Delta U_{Cp}(mT)$ in the vicinity of the time moment $(m-1)T + \tau$, i.e. before the moment of time $mT$, in the neighborhood of which the adjustable edge of the control pulse of the power switch is formed. Accordingly, the calculation of the dynamic component of the input signal of the pulse-width modulator in accordance with (2) and (3) can also be performed in the vicinity of the time instants $(m-1)T + \tau$.

4 Practical implementation

Figure 2 shows the block diagram of the control device which implements the discrete law (1) of the input PWM signal.
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5 Conclusion

The considered variant of realization (1) with application of discrete values of a regulated

compartment of an output voltage can be used to control power modules (PM), which are the

part of multimodular switching voltage regulator. The control device for PM, which is a

part of a multimodular switching regulator, in addition to the tasks to ensure the required

quality of the output voltage in dynamic and static modes of operation, must also solve the

problem of distributing the load current between the individual modules included in the

regulator. As a rule, the developers seek to ensure balancing distribution of the load current

between the PMs, which allows to equalize the heat dissipation in the modules. For an even

distribution of the load current between the PMs in the analog realization of the control

device, a negative feedback loop (FL) is often used for the inductor current. The application

of this circuit forms an oblique external characteristic of the module, which helps to balance

the output currents of the PM.

In the course of tests to provide the functions (logic of operation) and the requirements

of operability (fault tolerance), it was established that the layout of the digital control

module completely confirmed the declared technical characteristics.

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