

Development of Op-Amp Based Piezoelectric Rectifier for Low Power Energy Harvesting Applications

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Abstract. In this study, the development of operational amplifier (op-amp) based rectifier for piezoelectric energy harvesting applications was studied. The two stage op-amp full wave rectifier was used to convert the AC signal to DC signal voltage received by piezoelectric devices. The inverted half wave rectifier integrated with full wave rectifier were designed and simulated using MultiSIM software. The circuit was then fabricated onto a printed circuit board (PCB), using standard fabrication process. The achievement of this rectifier was able to boost up the maximum voltage of 5 V for input voltage of 800 mV. The output of the rectifier was in DC signal after the rectification by the op-amp. In term of power, the power dissipation was reduced consequently the waste power decreases. Future work includes optimization of the rectifying circuit to operate more efficiently can be made to increase the efficiency of the devices.

1 Introduction

Energy harvesting is a process that captures small amounts of energy that would otherwise be lost as heat, light, sound, vibration and movement. Piezoelectric energy harvesting, has been investigated since the late 1990s [1]. Energy harvesting technology has ability to create autonomous, self-powered electronic systems that do not rely on battery power for their operation [2]. There are available energy from the environment that allow self-sufficient energy to supply to low power consumption load such as sensor or radio transmitters. This available energy needs to convert before being used. The kinetic energy converts to electrical energy by using the piezoelectric device (PD) as a transducer.

In order to convert kinetic energy to electrical energy, some vibration or shocks need to apply on the surface of piezoelectric devices, then it will mechanically deform the piezoelectric crystal with tension or pressure to generate electrical charges. The electrical charges that being generated can measure as voltage. The circuitry of the energy harvesting connected to the piezoelectric devices by using the operational amplifier (op-amp) based rectifier to amplify the output and give the better desired output to the system.

The power dissipation will reduce as the waste power decreases. The rectifier used to convert the alternating current signal voltage to direct current signal voltage. It connected with the piezoelectric devices as a source energy, then flow through the rectifier. Based on the

piezoelectric device, it is capable to generate AC signal from the vibration or movement that being apply on it. The AC signal from piezoelectric device converts to DC signal using rectifier.

In order to maximize the power transfer of the transducer to the load, rectifier will be the most important part in the piezoelectric energy harvesting. A recent method of rectifier used comparator-based active diode, but it faces oscillation problem and dc offset. This problem cause the lower power efficiency of the rectifier [3]. In this paper the rectifier used the active diodes based on op-amp. The reason for using the op-amp rectifier compared to other rectifier because it reduces the power dissipation and increase the power efficiency.

In this study, the design and development of op-amp based rectifier for low power energy harvesting applications reported. The rectifier simulated using MultiSIM software and the characterization carried out in order to check capability of the circuit to work as energy harvesting circuit.

2 Research Method

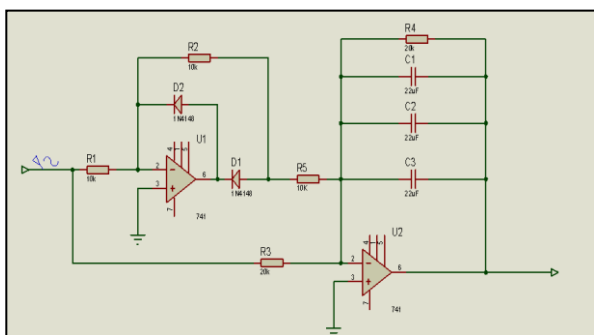
In this study, two stage op-amps full wave rectifier circuit has been proposed to convert the AC signal to DC signal. Figure 1 shows the schematic circuit and printed circuit board (PCB) of Op-amp based rectifier.

For negative input voltage, the output first Op-amp should be positive, then the D_1 ON and D_2 OFF. Since, action of the first Op-amp is forcing the input

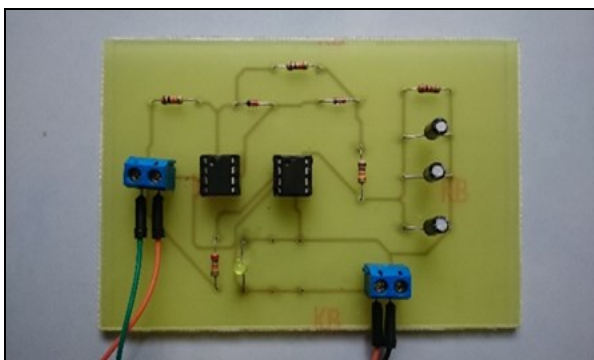
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voltage of the Op-amp to the same level, the output is hold at the ground potential when the D_1 is ON. For positive input signal V_i , the output goes negative, the D_2 is ON and D_1 is OFF. The output of first Op-amp then acts as inverting amplifier. The gain set to 1 by R_2/R_1 which means $R_2 = R_1$. The output of first Op-amp is 0V follow the negative half cycle of the input for positive half cycle. This output becomes one of the input for the second Op-amp which summed with input waveform. At the first stage it has 2 relative gains to the input which means $R_3 = 2R_4$

For second stage, at negative half cycle, the input wave summed with potential ground result the output of second Op-amp is a positive gain. The inverted wave of the half wave rectifier summed with input waveform and the gain of the half wave rectifier of first Op-amp for the positive half cycle results the positive output. The summer performs as an inverted again giving $V_o = |V_i|$ as shown in Figure 2 and Figure 5. The result is a full wave rectifier. Typically the gain of the half wave rectifier is set to 1 which are $R_2 = R_1$ and ratio of R_3 and R_4 should be 1:2 so the summing of the input with the output of the half wave rectifier output will be sum up correctly. The light emitting diode (LED) used as a load in this rectifier circuit.



(a)



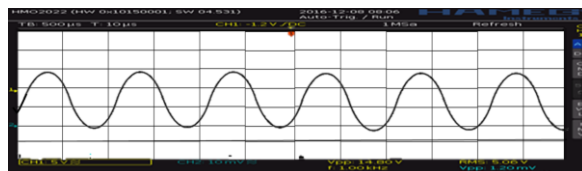
(b)

Fig 1. (a) Schematic and (b) PCB of op-amp based rectifier circuit.

3 Result and Discussions



(a)



(b)

Fig 2. a) The simulation and b) experimental input waveform of the Op-amp based rectifier.

Based on the arrangement of the circuit, the input V_{rms} set at 5V at the source of the circuit. The gain of the inverting op-amp is 1. As shown in Figure 2 the amplitude of V_{rms} output is 5V. There are no different between the simulation and experimental.

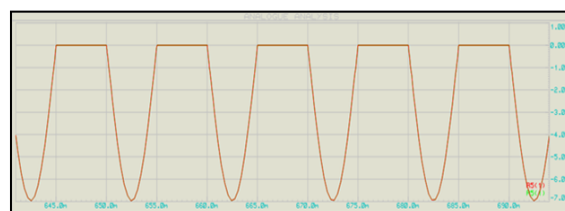
$$A_v = -\frac{R_2}{R_1} = \frac{10k\Omega}{10k\Omega} = -1 \quad (1)$$

By calculation the value of V_{out} :

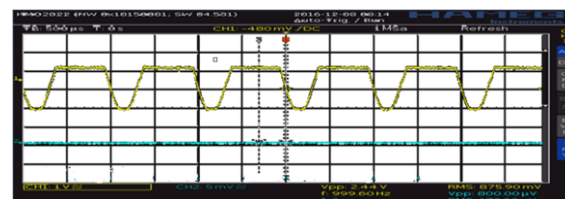
$$A_v = \frac{V_{out}}{V_{in}} \quad (2)$$

$$1 = \frac{V_{out}}{V_{in}} \quad (3)$$

$$\therefore V_{out} = 5V$$



(a)



(b)

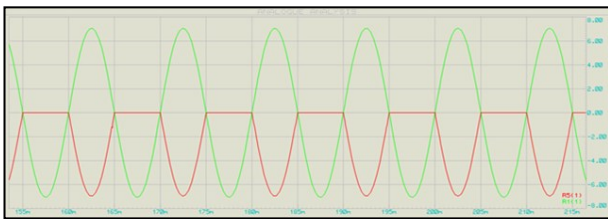
Fig 3. a) The simulation and b) experimental inverted half-wave rectified waveform of output by first Op-amp.

Since inverting Op-amp used, the output waveform inverted as the output of the first stage of Op-amp is half wave. During the negative half-cycle of a sinewave input, the output should positive. During the positive half-cycle, the output should zero. The circuit that accomplishes this amazing feats looks like the inverting amplifier with a couple of diodes added because only one actually does the rectifying action.

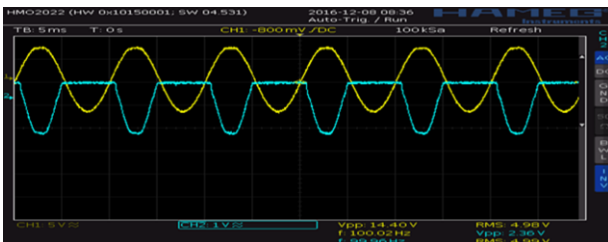
The other simply keeps the Op-amp in control while the signal output holds at zero. For the positive half-cycle input, the Op-amp output goes negative forcing D_2 to turn ON and D_1 to shut OFF. The output sits at 0V. This is because the Op-amp still does its job of holding V^- at 0V and because there is no current through R_2 and get $V_o = V^- = 0V$. The only thing left to reckon with is the current running through R_1 :

$$I = \frac{V_i}{R_2} = \frac{1}{10k} = 0.5mA \quad (4)$$

During this half-cycle, the Op-amp's output swings negative to turn on D_1 enough to pull 0.5 mA through the diode.



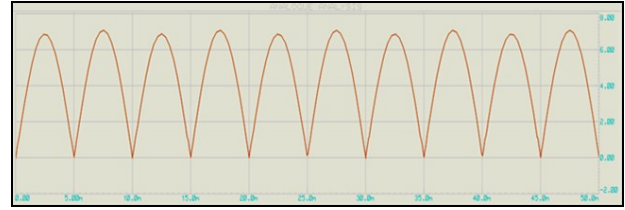
(a)



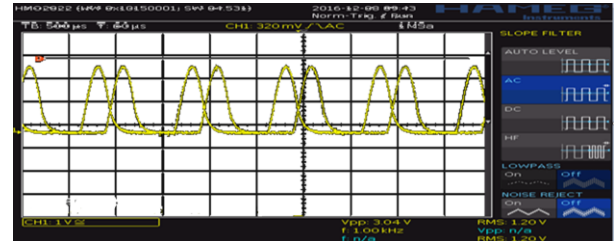
(b)

Fig 4. a) The simulation and b) experimental inverted half-wave with the input waveform.

As shown in Figure 4. The amplitude of the V_{rms} is 5V that equal to the $V_{p-p} = 7.07V$ due to the gain of the operational amplifier.



(a)



(b)

Fig 5. a) The simulation and b) experimental Full Wave rectified waveform of output by second Op-amp.

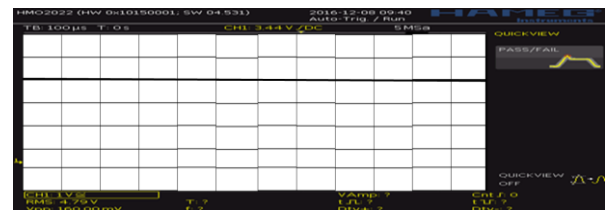
Two separate circuits used for each half of the full AC cycle and combined signal used to get the full wave rectified output [4]. From Figure 5, the output waveform generated by the second Op-amp. The output voltage V_{out} form the second Op-amp are $7.07V_{p-p}$ that equivalent to 5V of V_{rms} . There are different in phase shift when compared between the simulation and experimental.

$$V_{out} = -R_f \left[\left(\frac{V_{in1}}{R_5} \right) + \left(\frac{V_{in2}}{R_3} \right) \right] \quad (5)$$

$$V_{out} = -20k\Omega \left[\left(\frac{1.05mV}{10k\Omega} \right) + \left(\frac{7.07V}{20k\Omega} \right) \right] = 7.07V \quad (6)$$



(a)



(b)

Fig 6. a) The simulation and b) experimental output of Op-amp based rectifier.

The output of Op-amp in Figure 6 rectified by 22uF capacitor filter. 3 units of capacitor used to rectify the peak of the full wave. As the input and current was 1.05mA and 1.0mA respectively, the V_{p-p} of the rectified waveform is:

$$V_{p+} + V_{p-} = V_{p-p} \tag{7}$$

$$4.90 + 4.79 = 9.69V \tag{8}$$

The V_p of the waveform:

$$\frac{V_{p+} + V_{p-}}{2} = 4.845V \tag{9}$$

The V_{DC} of the waveform:

$$\frac{4.90V + 4.79V}{2} = 4.845V \tag{10}$$

Therefore, the efficiency of rectifier:

$$Efficiency, \eta = \frac{P_{outDC}}{P_{inAC}} \times 100\% \tag{11}$$

$$P_{outDC} = V_{outDC} \times I_{outDC} \tag{12}$$

$$P_{outDC} = (4.845V \times 1mA) = 4.845mV \tag{13}$$

$$P_{inAC} = V_{rms} \times I_{rms} \tag{14}$$

$$P_{inAC} = (5V \times 1.05mA) = 5.25mW \tag{15}$$

$$Efficiency, \eta = \frac{4.845mV}{5.25mV} \times 100\% \tag{16}$$

$$\therefore 92.28\%$$

The output DC voltage of the Op-amp based rectifier is 4.845V. Compare to the simulation output is 4.35V. The voltage dropped through the circuit since LED used as the load of the circuit design.

Table 1 shows the comparison between this proposed method and previous proposed method. The input voltage for proposed design is 5V in AC and the voltage output is 4.845 V in DC measured. The proposed design was using the full wave rectifier based on op-amp with the capacitor filter and achieved the highest power efficient of 92.28%. Although the proposed method also used the operational amplifier as a rectifier but with some modification made, it achieved the highest value of voltage output in DC, power output in DC and power efficiency.

Table 1. Comparison with previous rectifier method

Parameter	Lam Rectifier	Synchronized Switching Rectifier	A Self-Powered High-Efficiency Rectifier	Proposed Method
V_{outDC} (V)	2.78	1.8	2.9	4.845
P_{outDC} (mW)	0.081	0.143	0.090	4.854
η (%)	90.00	77.00	91.20	92.28

4 Conclusion

In this paper, the development of Op-amp based rectifier circuit has been present. The design circuit utilizes two of Op-amps with the capacitor filter through precise in rectification of the waveform. The configuration gives the advantages of precise rectification over a wide range of operating frequencies, a high input impedance and the ability to give the less power dissipation. An improved rectifier circuit employing the principles similar from the previous research.

References

1. S. Bhalla, S. Moharana, N. Kaur, V. Talakokula, *Piezoelectric Materials*, (John Wiley & Sons, London, 2016)
2. M.N. Gupta, S.K. Yadav, Suman, *AEEE*, **4**, 313 (2014)

3. T. Gunasheela, E.K .Sharmitha, K. Senthilprakash, *An Efficient Rectifier Design for Piezo Electric Energy Harvesting System* (ICSEIE, Erode, 2015)
4. R. Dixit, C. K. Dwivedi, IJETAE, **4**, 388 (2014)
5. Y. Sun, N. H. Hieu, C.J. Jeong, S.G. Lee, IEEE Trans. Power Electron. **27**, 623 (2012)
6. X. D. Do, H. H. Nguyen, S.K. Han, D. S. Ha, S.G. Lee, JSTS, **15**, 319 (2015)
7. X. D. Do, H. H. Nguyen, S.K. Han, D. S. Ha, S.G. Lee, IEEE Trans. Very Large Scale Integr. (VLSI) Syst. **23**, 444 (2015)