Improved Greedy Algorithm Based Matrix Satellite Cable Connection Optimal Design

Yiyi Wang¹, Zhifu Wang¹, Ke Zong¹, Mian Ye¹, Xi Zhou¹ and Zhijie Wang¹

¹ Institute of Telecommunication Satellite, China Academy of Space Technology, Beijing 10094, China

Abstract. An improved greedy algorithm based matrix satellite cable optimal design method is presented in this paper aim to overcome the difficulties including designing complexity, designing cycle and iterative designing process. An optimal algorithm model is built to help designer not only to accomplish design correctly and efficiently, but also to obtain better designing outcomes. Firstly, matrix cable designing model is pre-translated by turning electrical interface to equivalent digital model. Then the digital model is turned into computer language. At last, optimal design rules and method are extracted according to improved greedy algorithm. Real project data are used to prove the feasibility and validity of the proposed method.

1 Introduction

Matrix circuit including matrix telemetry and command is a core technology of on-board integrated electronic technology [1] to reduce the amount of electronic components and cables. This technology is widely used in all kinds of satellites to observably lose the weight of satellite platform. The devices with matrix telemetry and command can be connected with integrated electronic subsystem by matrix cable to send commands and receive telemetries. Matrix cable design has great possibility to be improved by reducing the usage of cables because matrix circuit usually has a large number of brunches which may cause the redundancy of design.

Consequently, an improved greedy algorithm based matrix satellite cable optimal design method in presented in this paper aim to overcome the difficulties listed above. Computer based design can acquire correct and preferred outcome, meanwhile, improve the work efficiency.

2 Matrix cable contact design principle

Matrix command contact circuit comprises command drive device and command load device. Command drive device can control m×n pieces of load device by control m row cables and n column cables [2]. It takes 2(m+n) cables to control m×n pieces of load device if backup is included. Matrix telemetry contact circuit is using the same principle. Matrix cable contact design principle is to build a point-to-point mapping relation between drive device model and load device model. If NRC=

3 Improved greedy algorithm based satellite matrix cable connection optimal design

1.1 Design model pre-processing

Circuit model is tuned into structural digital model to achieve optimal design. Assuming that a command load device has 9 matrix commands controlled by 3 row cables and 3 column cables, it can be shown as

MRC=

Similarily, drive device can be shown as

From the knowledge mentioned above, row 1 and column 1 take control of M11. According to the principle, circuit connection can be translate into mapping relation between drive device model and load device model. If NRC=

can be shown as

1.2 Formula expression of design model

* Corresponding author: wyywangyi1@126.com

© The Authors, published by EDP Sciences. This is an open access article distributed under the terms of the Creative Commons Attribution License 4.0 (http://creativecommons.org/licenses/by/4.0/).
Formula expression of design model is \( \{ Rx, Cy, k \} \).

\( Rx \) represents for row cable, \( Cy \) represents for column cable, \( k \) represents for the number of matrix model on one device.

1.3 Improved greedy algorithm based matrix cable connection optimal design method

Optimal design is achieved by algorithm based programming on the basis of digital model and formula expression input.

A greedy algorithm does not consider the overall result, but aim to find a partial optimum. In most cases, a greedy strategy follows an optimal principle and takes partial optimum step by step. The greedy algorithm structure is as follows.

Beginning with an original solution for a problem,
While ( making one step forward to final goal )
{ 
Use available strategies to find a solution 
}
Get a final solution by combining all the solutions

VLSI routing problem [3] and 2D packing problem [4] chooses different row cable and column cable will get different matrix connections. Travelling salesmen problem [5] has capacity restriction and mutual restriction. Unlike VLSI or TSP problem, this case is a 2D vehicle routing problem [6] with confliction and capacity restriction. To solve this problem, this article raised an improved greedy algorithm with complexity of \( O(M*n) \). \( M \) represents the maximum of row or column, \( N \) represents the amount of matrix.

1.3.1 Optimal Design rules summary

**Step 1** List all Matrix signals. \( \{ T \} \) is the set of telemetries; \( \{ C \} \) is the set of commands. \( \{ TM \} \) is the set of matrix telemetries; \( \{ CM \} \) is the set of matrix commands. \( T_M \in T \), \( C_M \in C \), \( \{ TM \} \) and \( \{ CM \} \) should be managed separately.

**Step 2** Separate all devices into different sections. Put devices into corresponding section lists by recognize their coordinates.

\[
T_M \begin{cases}
\text{South Part} & \{ +X \} \\
\text{North Part} & \{ -X \}
\end{cases},
C_M \begin{cases}
\text{South Part} & \{ +X \} \\
\text{North Part} & \{ -X \}
\end{cases}
\]

**Definition 1** \( T_{\text{Max}} \) is the matrix telemetry with maximum of row or column in \( \{ TM \} \).

**Definition 2** \( C_{\text{Max}} \) is the matrix command with maximum of row or column in \( \{ CM \} \).

**Definition 3** \( T_{\text{Sum}} \) is the matrix telemetry with maximum sum of row and column in a specific set of telemetries.

**Definition 4** \( C_{\text{Sum}} \) is the matrix command with maximum sum of row and column in a specific set of commands.

**Step 3** Find \( T_{\text{Max}} \) and \( C_{\text{Max}} \). If there are more than one \( T_{\text{Max}} \) or \( C_{\text{Max}} \), we get \( \{ T_{\text{Max}} \} \) or \( \{ C_{\text{Max}} \} \). Then we find \( T_{\text{Sum}} \) or \( C_{\text{Sum}} \). If there are more than one \( T_{\text{Sum}} \) or \( C_{\text{Sum}} \), the priority is random.

**Rule 1** Using row cable is prior to column cable if the total increment and partial increment are fixed.

**Definition 5** If one device has been appointed to be \( D_{\text{Aim}} \), algorithm cannot move on to other device until all the matrix signals are allocated.

**Definition 6** The set of all matrix telemetries of \( D_{\text{Aim}} \) except \( T_{\text{Max}} \) is \( T_{\text{Next}} \). The set of all matrix commands of \( D_{\text{Aim}} \) except \( C_{\text{Max}} \) is \( C_{\text{Next}} \).

**Step 4** When \( T_{\text{Max}} \) or \( C_{\text{Max}} \) is chosen, the device \( T_{\text{Max}} \) or \( C_{\text{Max}} \) becomes \( D_{\text{Aim}} \). \( T_{\text{Next}} \) of \( C_{\text{Next}} \) must be assembled with \( T_{\text{Max}} \) or \( C_{\text{Max}} \) to get the minimum sum of rows and columns in order to use cables as less as possible.

**Definition 7** The set of devices on the same branch of \( D_{\text{Aim}} \) except \( D_{\text{Aim}} \) is \( \{ D_{\text{Next}} \} \).

**Definition 8** \( T_{\text{Aim}} \) or \( C_{\text{Aim}} \) is the matrix signal which is going to be allocated then.

**Step 5** When finished \( D_{\text{Aim}} \), repeat step 3 and 4 for \( \{ D_{\text{Next}} \} \).

**Rule 2** The second junction point is the flag of where a branch starts.

**Rule 3** If there are more than one position with minimum cost increment, length increment is the solution to find the best choice.

**Step 6** When \( \{ D_{\text{Next}} \} \) is finished, repeat step 1 to 5 for the rest.

**Rule 4** Choose matrixes which have the same structure as \( T_{\text{Aim}} \) or \( C_{\text{Aim}} \) to assemble if priority cannot be recognized so that unnecessary blank can be avoided as far as possible. This rule is also made to reduce the usage of cable and shorten cable length.

**Step 7** After all matrix signals were allocated, do a final check to see if any adjustment can be done to get a better result.

1.3.2 Improved greedy algorithm based matrix cable connection optimal design

**Definition 9** \( S_{R/C} \) is the capacity of matrix cable allocation.
**Definition 10** $\Delta \text{Cos}$ is the cost increment when choosing position.

**Definition 11** $\Delta \text{Length}$ is the length increment.

**Definition 12** P is insertion point. P is on the first line and first row of $T_{\text{aim}}$ or $C_{\text{aim}}$.

If $S_{R}=C$, $C_{16 \times 24}$, 16 row lines and 24 column lines can be shown in a sketch map as follows. In figure 1, judgment flags as TRUE or FALSE are set at junction point, such as M, N, P.

According to figure 1, $C_{\text{Max}}=\begin{bmatrix} C & C & C \\ C & C & C \\ C & C & C \\ C & C & C \end{bmatrix}$ and $\{C_{\text{Next}}\} = \begin{bmatrix} C_{\text{N1}} \\ C_{\text{N1}} \end{bmatrix}_{1 \times 2}$. We can get $\begin{bmatrix} C & C & C & C \\ C & C & C & C \\ C & C & C & C \\ C & C & C & C \\ C & C & C & C \\ C & C & C & C \\ C & C & C & C \\ C & C & C & C \end{bmatrix}$ to make $\Delta \text{Cost}$ least. Now, $C_{\text{Max}}=\begin{bmatrix} C & C & C \\ C & C & C \\ C & C & C \\ C & C & C \end{bmatrix}$. P has 10 probable positions as $P, P, P, P, P, P, P, P, P, P$. To make both $\Delta \text{Cos}$ and $\Delta \text{Length}$ least, the final position is $\begin{bmatrix} C_{\text{Max}} & C_{\text{Max}} & C_{\text{Max}} & C_{\text{Max}} \\ C_{\text{Max}} & C_{\text{Max}} & C_{\text{Max}} & C_{\text{Max}} \\ C_{\text{Max}} & C_{\text{Max}} & C_{\text{Max}} & C_{\text{Max}} \\ C_{\text{Max}} & C_{\text{Max}} & C_{\text{Max}} & C_{\text{Max}} \\ C_{\text{N1}} & C_{\text{N1}} & C_{\text{N1}} \end{bmatrix}$.

Total cable length that $C_{\text{Max}}$ used: $8 \times QA = 8 \times 8 = 64$

**Figure 1** Cable layout sketch map

$C_{\text{N1}}$ and $C_{\text{Max}}$ belong to the same device, they share same column lines. $C_{\text{N1}}$ brings a new row line. So $\Delta \text{Cos}=1$, $\Delta \text{Length}$, $QA=8$. Then $C_{\text{N2}}$ and $C_{\text{Max}}$ belong to the same device, $C_{\text{N2}}$ share the same column lines with $C_{\text{Max}}$, meanwhile, share the same row line with $C_{\text{N1}}$. So $\Delta \text{Cost}=0$, $\Delta \text{Length}=0$. Total length of $D_{\text{aim}}$ is $64+8=72$.

In $\{D_{\text{Next}}\}$, $C_{\text{Max}}=\begin{bmatrix} C & C & C \\ C & C & C \\ C & C & C \\ C & C & C \end{bmatrix}$. $\Delta \text{Cost}=0+3=3$, $\Delta \text{Length}=4 \times NB + 3 \times BQ = 4 \times 8 + 3 \times 14 = 32 + 52 = 84$.

After $D_{\text{aim}}$ is allocated, the judgment flags of M and N turned to TRUE as shown in figure 2 because these junction points have been used once. When moving to next step, algorithm searches the first TRUE point to the direction back to the driver. At last, we got $\begin{bmatrix} c & c & c & c \\ c & c & c & c \\ c & c & c & c \\ c & c & c & c \\ c & c & c & c \\ c & c & c & c \\ c & c & c & c \\ c & c & c & c \end{bmatrix}$.

**Figure 2** Branch judgment flag

**4 Design example**

**4.1 Programming**

- **Interface 1** CountWidth
- **Interface 2** HeightComparator
- **Interface 3** Cell
- **Interface 4** SortCellUtil

The programming structure for interfaces is showed in figure 3.
The programming logic is shown in Figure 4.

![Diagram showing programming logic](image)

**Figure 4 Programming logic**

### 4.2 Real project data verification

Comparing with original design result, the optimal design result reduces total cable weight by 8% which is 0.416 kg.

### 5 Conclusion

An improved greedy algorithm based matrix cable connection optimal design method is proposed in this article and the method is tested by real data project to prove its reasonableness and effectiveness. The result shows that optimal design result reduced weight by 8% comparing with original design result. Some complicated situation may occur in real projects such as branch terminals and layout adjustments. Later we will make further improvement of this optimal design method.

### Acknowledgments

This work has been supported by Project of the Program of National Natural Science Foundation of China under the Grant Number 61471360.

This work has been supported by the 13th Five-Year Plan Pre-research special technology project from Space System Division.

### References

1. Mingfeng Li, Yanyan Hao, Xiao Chen. New telecommunication satellite integrated electronic system research [J]. Space International, 6, 46-50,(2013)