Simulation and Experiment on In-plane Carbon Nanotube Thermoelectric Generator in Parallel

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Abstract. In order to solve the problem of larger internal resistance of thin-film thermoelectric generator (TEG) in series, which could influence the output power and restrict the application, the in-plane carbon nanotube (CNT) TEG in parallel was ingeniously researched. Utilizing the parameters of output power, conversion and energy efficiencies, the ideal and actual models of TEG in parallel were established, respectively. The thermal conduction insulating layer was taken into account, which could provide the theoretical guidance for the experimental test and engineering applications. The CNT films were prepared by the floating-catalyst chemical vapor deposition (CVD), and the experiment and properties of TEG based on CNT films were investigated. The testing circuits of conventional and gas TEGs were designed, and the output powers of the serial and parallel connecting types were tested and compared. The correctness of theoretical model and numerical analysis was proved to be valid. The novel method could effectively enhance the output power, extend the applied range of TEG in MEMS/NEMS and had a fine prospect.

1 Introduction

Along the quick development of micro/nano systems and their extensive applications, the power source supplied by the storage battery cannot be adapted well to the rigorous environments and requests. The size of power sources will restrict the microminiaturization and practicability of micro/nano systems, which cannot be easily replaced by the battery. The power source supplied by the storage battery cannot be adapted well to the rigorous environments and requests. The sustainable self-sufficient micro-power source, which harvests energy from the environment to power the micro/nano systems, is a feasible method of solving the supply problems [1-3]. The thermoelectric generator (TEG) is highly desirable since it can convert the heat energy into the electrical power.

TEG has many advantageous qualities over alternative devices, e.g. comparatively lightweight, completely silent, highly reliable, clean energy, no moving parts and no consumables. The heat energy could be supplied from the solar energy, automobile exhaust, production waste gas or heat dissipation of electronic components [4, 5]. So it may play a key role for green energy production and utilization in the future. However, in order to make the TEG practical, more efficient thermoelectric materials and optimized structures must be developed and researched in recent years.

Since the discovery of CNT in 1991 [6, 7], the correlated research work has increased exponentially over the years owing to the splendid physical, chemical, structural and electrical properties [8,9]. It can generate electricity in the photic, thermic, magnetic or fluidic fields, which can be applied to a more complicated environment [10, 11]. Based on the thermoelectric effects, the thermal gas and solar energy from ambient environment can be converted to electric energy.

Recently, Ref. [11, 12] reported the thermoelectric properties of CNT films and designed an integrated device to directly convert illuminating NIR light into a notable output voltage. The p-type and n-type CNT films were arranged alternatively and linked with each other in series. The output voltage can reach 0.1 V by setting 50 p-type CNT films and 50 n-type CNT films. However, the thin-film TEG has a larger internal resistance due to the smaller cross-sectional area. The maximum power output is directly proportional to the Seebeck coefficient and temperature difference between the hot and cold ends. But it is inversely proportional to the internal resistance. The output electrical properties are greatly influenced by the larger internal resistance. Hence, thermoelectric module (TEM) by connecting diverse p-type and n-type thermoelectric cells in parallel can reduce the internal resistance and enhance the output power. It is significant to the TEG application in diverse fields.

2 Theoretical model

2.1. Ideal theoretical model

In the ideal conditions, suppose the temperatures of the hot and cold ends remains invariable. The convection and radiation heat transfer between the hot and cold ends are...
neglected. The heat mainly transfers along the couple arms. The electrical properties effected by the electrode resistance, contact resistance and thermal resistance are neglected. When the temperature difference between the hot and cold ends is minor, the Thomson effect could be neglected. The ideal schematic diagram of the membrane thermoelectric module in parallel is shown as Fig. 1(a).

![Schematic Diagram of Membrane Thermoelectric Module](image)

Every thermoelectric cell (TEC) includes P-type and N-type semiconductor couple arms and electrode layer. At the low temperature end, the P-type couples are connected on the substrate top surface. The N-type couples are connected on the substrate bottom surface. The membrane thermoelectric module in parallel, abbreviated ‘parallel TEM’ includes diverse TECs in parallel.

Figure 1. Ideal (a) and actual (b) schematic diagrams of membrane TEM in parallel.

Suppose that the parallel TEM is constituted by $m$ pairs of TECs. Every TEC has the same material properties and temperatures at the cold and hot ends. So the CNT couple arms have the same electrical currents $I_i$. The current in the main circuit is $I = mI_i$. The heat quantity exchanged between the heat source and hot ends of TEC is approximately equivalent.

The output power expressed by the current in the main circuit is

$$P = \frac{Q_h - Q_c}{m} = \frac{\alpha_{\text{m}} I \Delta T - I^2 r}{m}$$

(1)

The conversion efficiency is

$$\eta = \frac{Q_h - Q_c}{Q_h} = \frac{m \alpha_{\text{m}} I \Delta T - r I^2}{m^2 K \Delta T + m \alpha_{\text{m}} T^2 I - 0.5 I^2 r}$$

(2)

### 2.2. Actual theoretical model

When the thermal conduction insulating layer is considered in the parallel TEM, the actual schematic diagram is shown as Fig. 1(b). The positive-negative electrodes are located at the low-temperature ends. The parallel TEM requires that the P-type couple arms are connected and the N-type couple arms are connected together. So the metal wire will slightly influence the electrical properties, whether the metal wire drills through the thermal conduction insulating layer or substrate material layer. The $m$ pairs TECs in plane substrate are equivalent to the thickness of CNT films increased by $m$ times.

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The output power expressed by the current in the main circuit is

$$P = [\alpha K_s K_i \Delta T - r K_s K_i I^2/n - 2r K K_i I^2 - \alpha^2 I^2(K_s T_i + K, T_0) + 0.5 \alpha r \Delta K K_i I^2/n] \times [K_s K_i + 2nK K_i - \alpha n \Delta K K_i I - n^2 \alpha K K_i J^2 I^2]$$

(3)
The conversion efficiency can be expressed as

$$\eta_c = \frac{\alpha K_h K_c \Delta T_1 I - 2 r K_h I^2 + 0.5 r K_h I^2/n \times K_h}{\alpha K_h K_c \Delta T_1 I - (0.5 r K_h/n + r K_h + \alpha T_1 I^2) + 0.5 r I^2/n} \times (n K_h K_c \Delta T_1)$$

(4)

The energy efficiency can be got by the equation below

$$\eta_e = \frac{\eta_c I}{\Delta T_1}$$

(5)

When the temperature difference is smaller, the performance parameter is propitious to analyze the thermoelectric properties.

### 3 Simulation and Analysis

Assume the temperature $T_1=298$ K, $T_2=393$ K, the internal resistance of TEC $r=10$ Ω, the performance analysis of membrane TEM in parallel with an ideal model is shown as Fig. 2(a) and (b). Along with the increase of parallel pair $m$, the whole internal resistance of TEM obviously decreases. The output power can be enhanced by configuring the load resistances. When the internal resistance is equal to the load resistance, the output power reaches the maximal value. The maximal output power of series TEM is equal to the one of parallel TEM when they have the same connection pairs. The maximal value of conversion efficiency or exergy efficiency is independent of the parallel pair, the values are close to the ones of serial TEM.

Assuming the coefficient of thermal conductivity at the hot and cold ends are respectively $K_h=0.8$ mW/K, $K_c=0.4$ mW/K. In order to simplify the calculation, assume the surface heat transfer coefficient of TEM is fixed, $K_h$ and $K_c$ are directly proportional to the parallel pair. The performance analysis of membrane TEM in parallel with an actual model is shown as Fig. 2(c) and (d). The output power and the conversion efficiency could reduce due to the influence of the thermal conduction insulating layer. When $m=8$, the output power is 36.73 μW, the conversion efficiency is 0.1669%. When the pair of TECs is equal, the output current has larger difference. So we can get the conclusion that the connecting type of TECs is determined by the load power consumption and the electrical current requested.

### 4 Experiment and verification

The in-plane TEG is composed of p-type and n-type CNT films connected by aurum electrodes. The insulator materials are filled in the gaps between p-type and n-type CNT films, which also have functions of reinforcing and smoothing the material surface. The CNT film is synthesized by a floating catalyst chemical vapor deposition (CVD) method [13, 14]. Its schematic diagram is shown in Fig. 3(a). The experimental setup is a three-section quartz tube mounted in a two-stage furnace system. The quartz tube is first flushed with the argon flow to exhaust the air. Ferrocene as catalyst precursor and sulphur as growth promoter are heated (60~80 °C) and carried into the reactor (1100 °C) along with the gaseous mixture of argon and methane. The large-area transparent CNT film will be synthesized in the quartz tube wall after five hours. The SEM morphology of CNT film is shown in Fig. 3(b). By doping n-type semiconductor element or coating PEI[15, 16], the n-type CNT film can be synthesized.

![Schematic diagram of CVD apparatus used for synthesizing CNT membrane (a), and SEM morphology of CNT membrane (b).](image-url)
CNT films are heated by the thermoprobe, the temperature is 348 K at the hot-end and 298 K at the cold-end. The temperature difference is $T = 50$ K, which is invariable in the experiment. As shown in Fig. 4(a) and (b). The maximum output powers of the TEM in series and parallel are $1.35 \times 10^{-3} \mu W$ and $1.24 \times 10^{-3} \mu W$. The output power could be greatly enhanced by increasing the temperature difference and connection pairs. In the experiment, the different maximum output power is attributed to the collocation method of TEC. The parallel TEM includes two TECs are respectively disposed on the top and bottom of substrates. The larger area of electrode layer has bigger contact resistance and thermal resistance, which influence the power output. The parallel TEM can enhance the output power by reducing the internal resistance and increasing the loop current. So it is more significant in the application of thin-film TEG.

### 5 Summary

In this work, the theoretical model and optimization techniques of conventional TEG based on CNT films are researched. Utilizing the parameters of output power, conversion efficiency and energy efficiency, the ideal model and actual model of TEM in parallel are respectively established. The thermal conduction insulating layer is taken into account, which can provide the theoretical guidance for the experimental test and engineering applications. The experiment and properties of TEG based on CNT films are researched. The CNT films are prepared by CVD method. The testing circuits of conventional and gas TEG are both designed. The output voltage and power are tested in the series-parallel connecting types. The correctness of theoretical model and numerical analysis is proved to be valid. It can achieve the waste heat recovery from the solar energy, automobile exhaust, production waste gas or heat dissipation of electronic components to self-power MEMS/NEMS in the future.

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### References